



D.F.W.

Attorney Docket No. 5649-1232

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Su-Jin Ahn et al.

Serial No.: 10/763,016

Filed: January 22, 2004

For: SEMICONDUCTOR DEVICES WITH SCALABLE TWO TRANSISTOR
MEMORY CELLS

Group Art Unit: 2818

Examiner: Tu-Tu V. Ho

Confirmation No.: 8009

October 22, 2004

Mail Stop AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT AND
RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

In response to the Restriction Requirement of September 28, 2004, Applicants hereby elect the invention of **Invention I** which is drawn to Claims 1-17.

Applicants are amending the Title to reflect the above-identified election.

It is not believed that an extension of time and/or additional fee(s), including fees for net addition of claims-are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.

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Page 2 of 7

In the Title:

Please delete the Title in all occurrences and substitute the following Title therefor: --
SEMICONDUCTOR DEVICES WITH SCALABLE TWO TRANSISTOR MEMORY
CELLS--.